

FIG. 1A

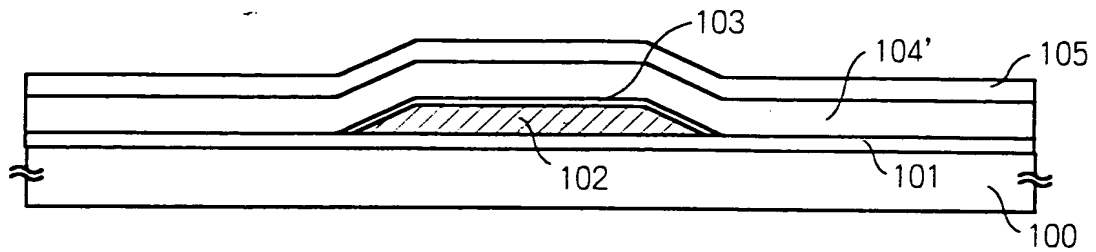


FIG. 1B

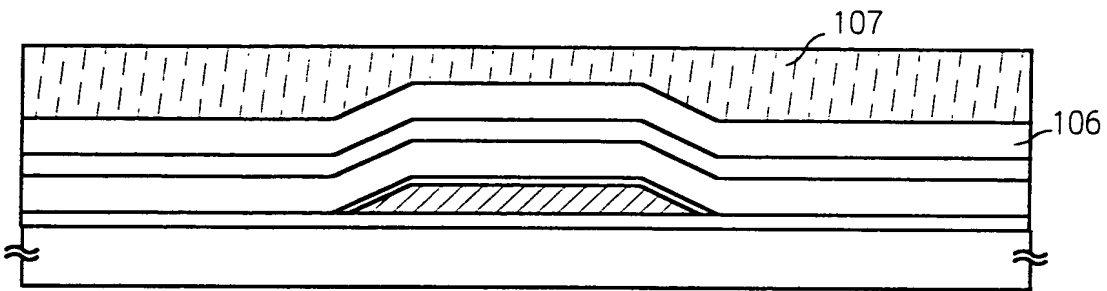


FIG. 1C

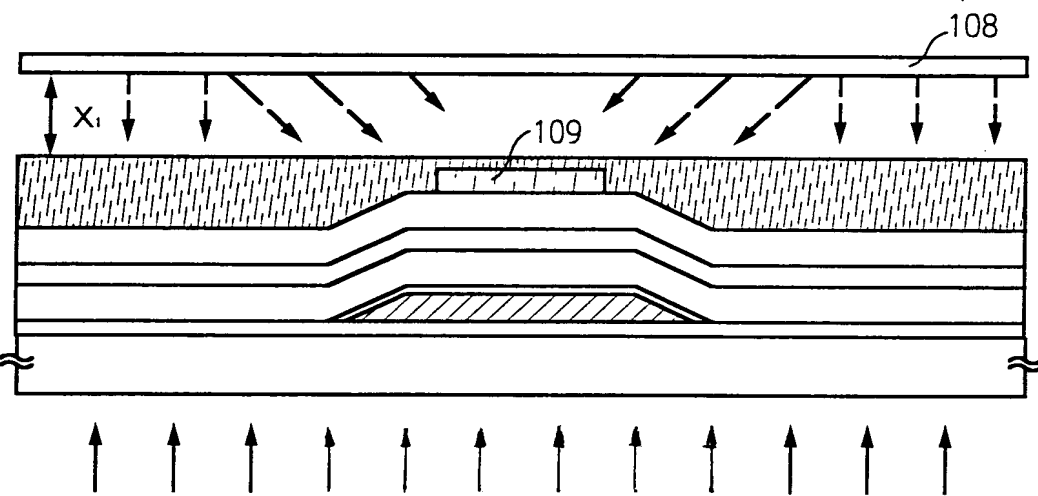


FIG. 1D

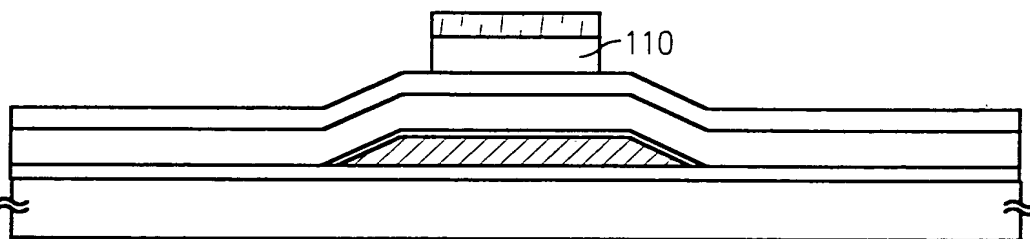


FIG. 1E

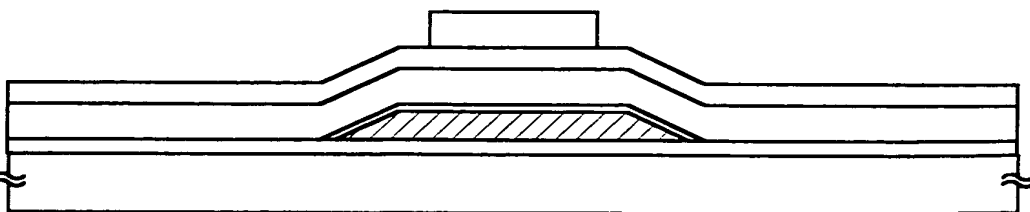


FIG. 2A

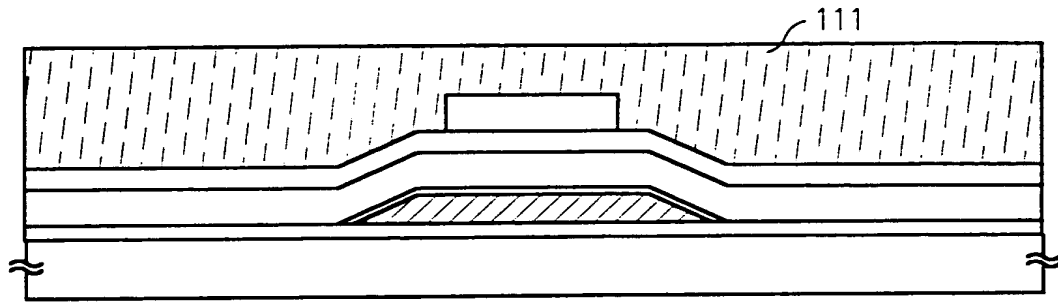


FIG. 2B

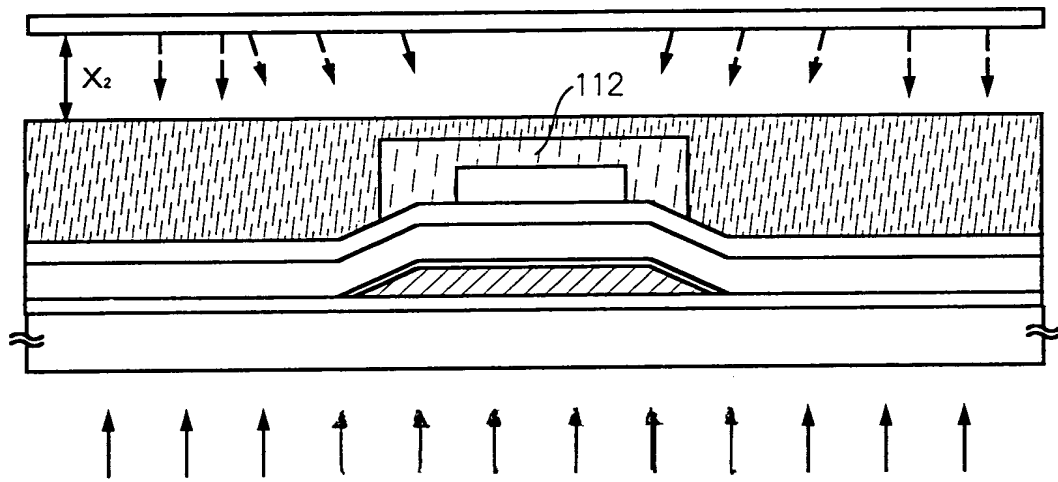


FIG. 2C

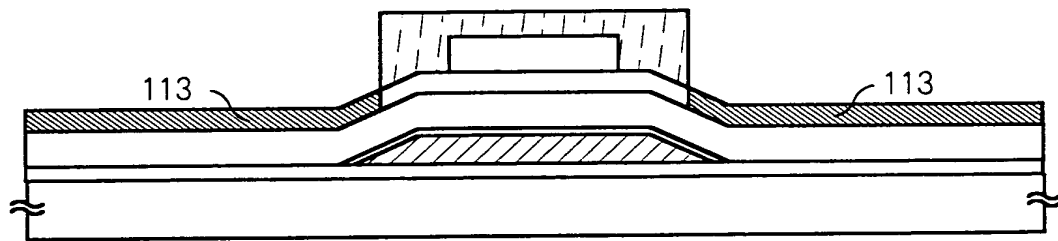


FIG. 2D

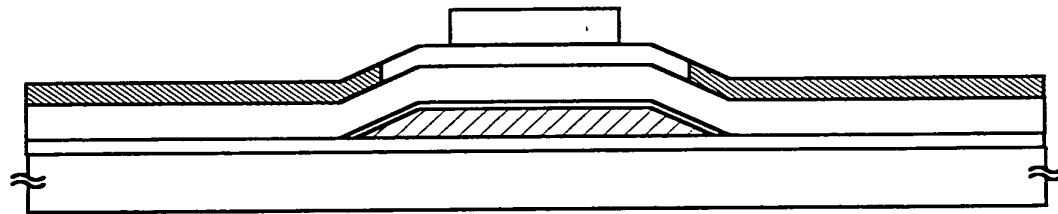


FIG. 3A

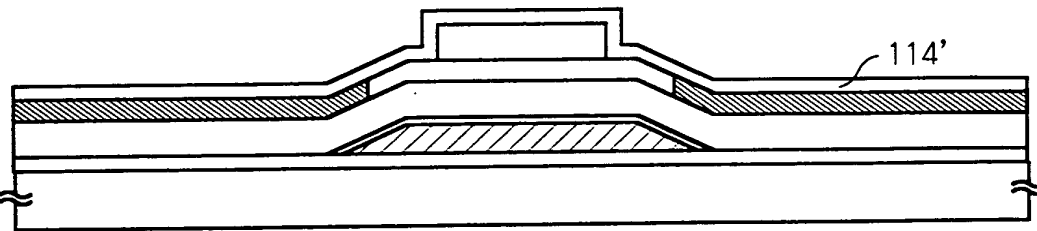


FIG. 3B

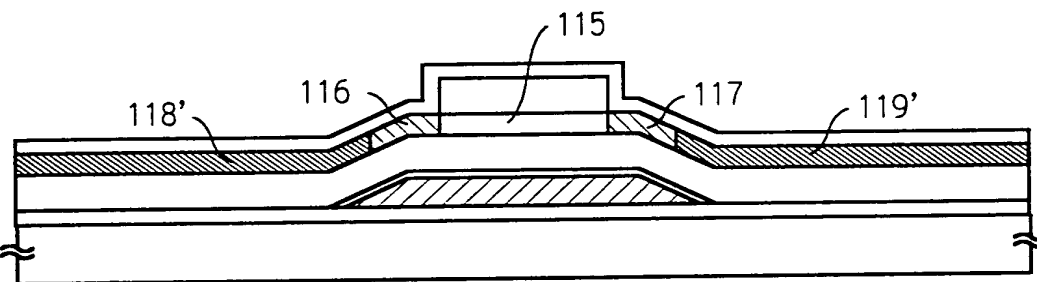
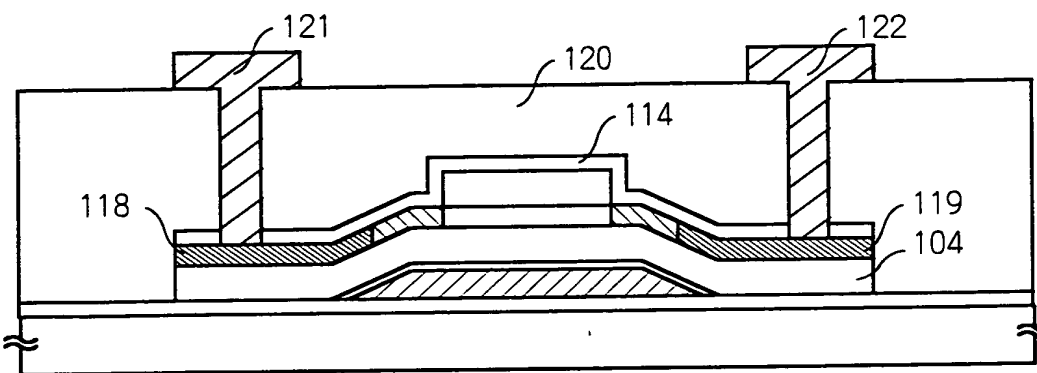


FIG. 3C



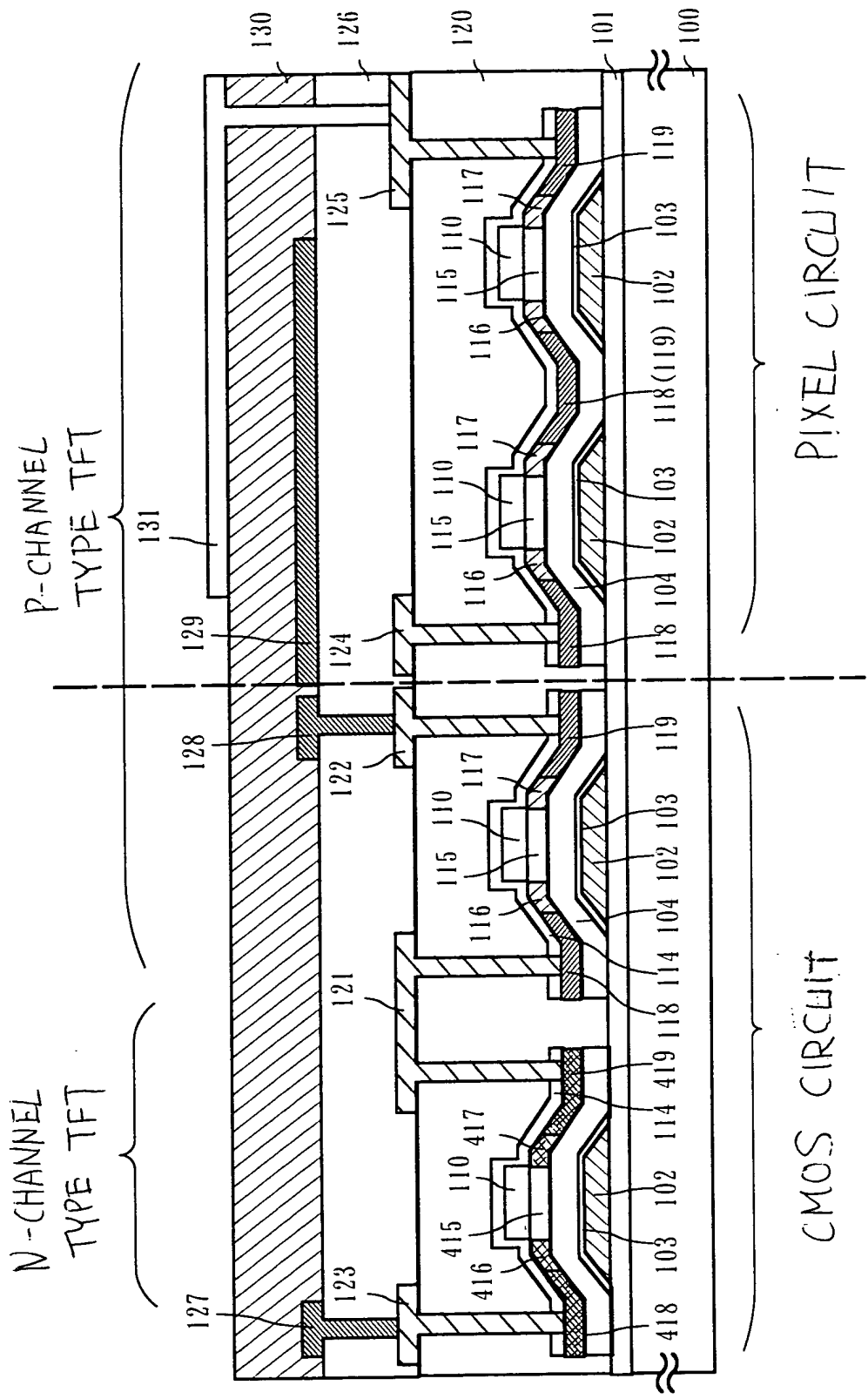


FIG. 4

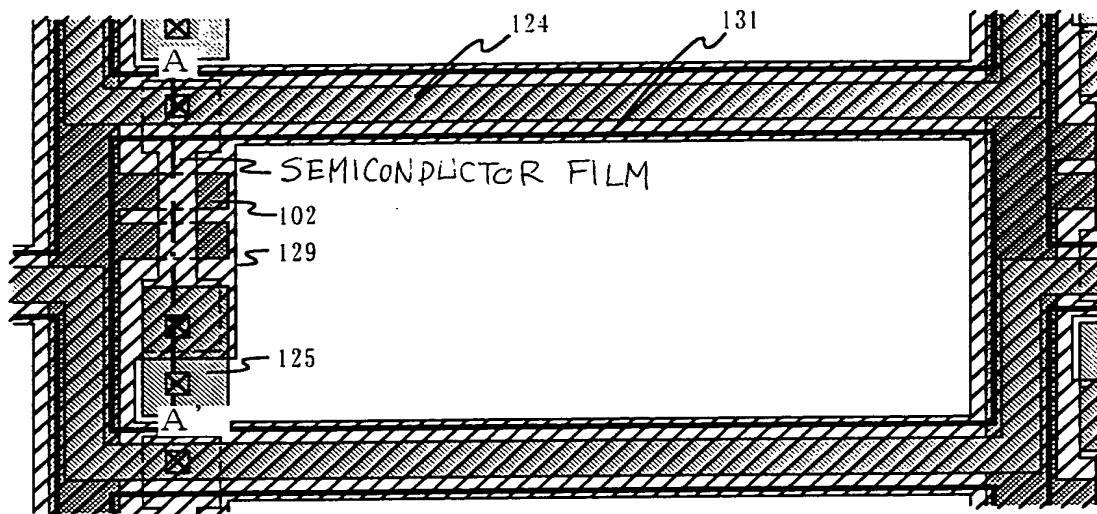


FIG. 5A

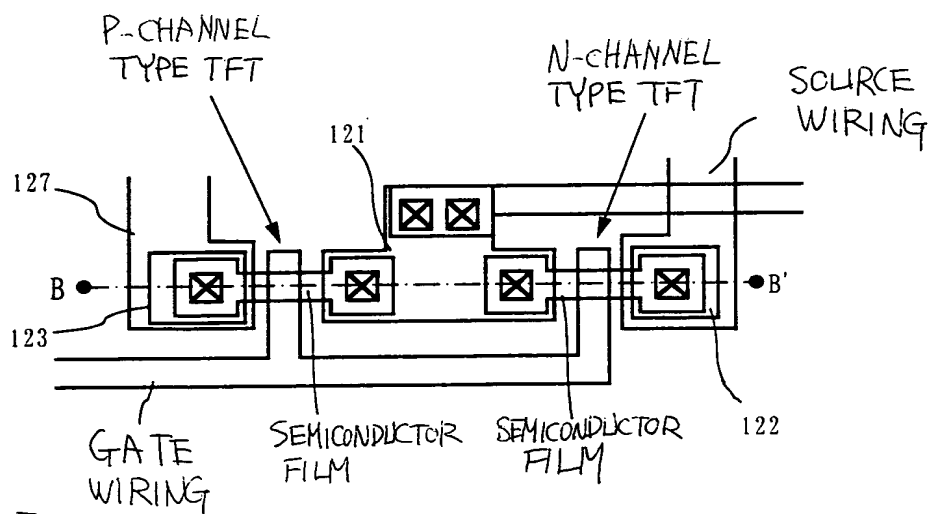


FIG. 5B

D

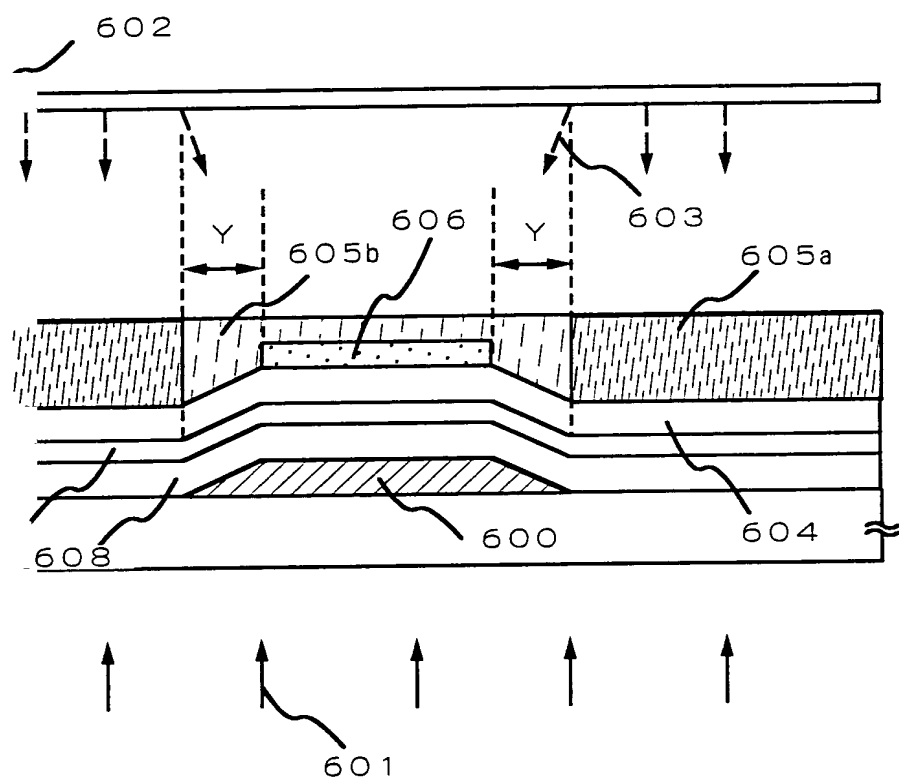


FIG. 6

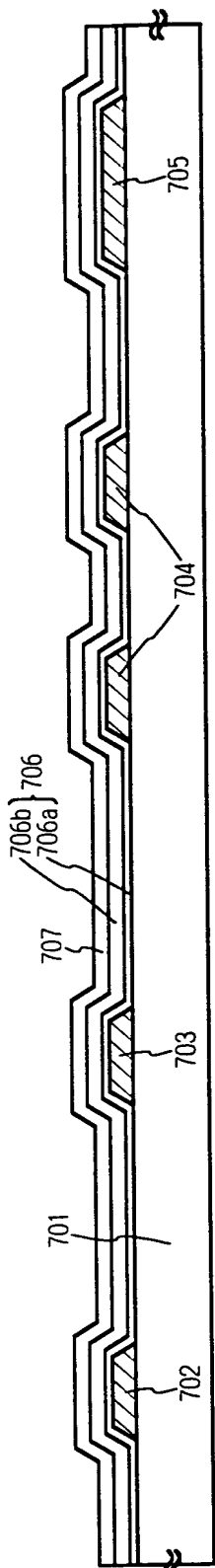


FIG. 7A

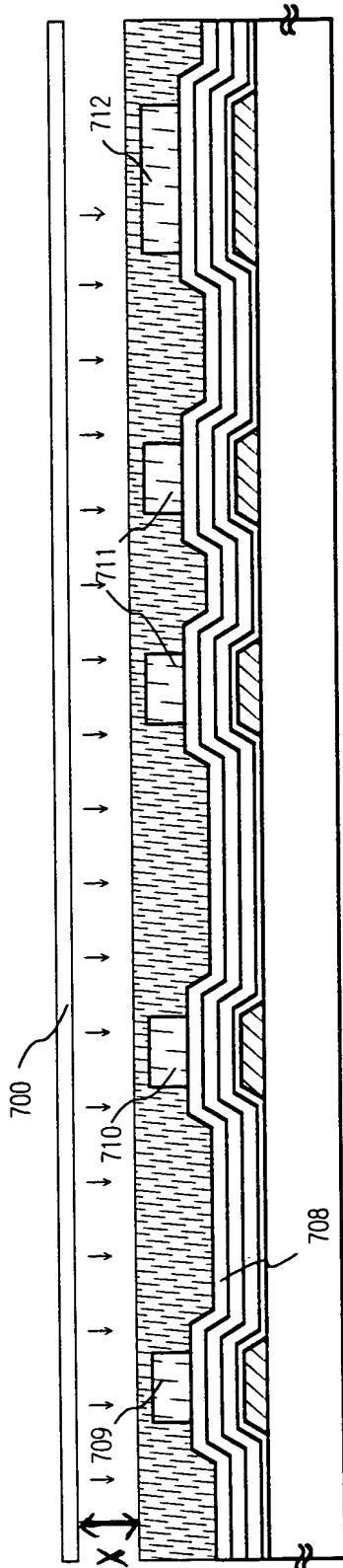


FIG. 7B

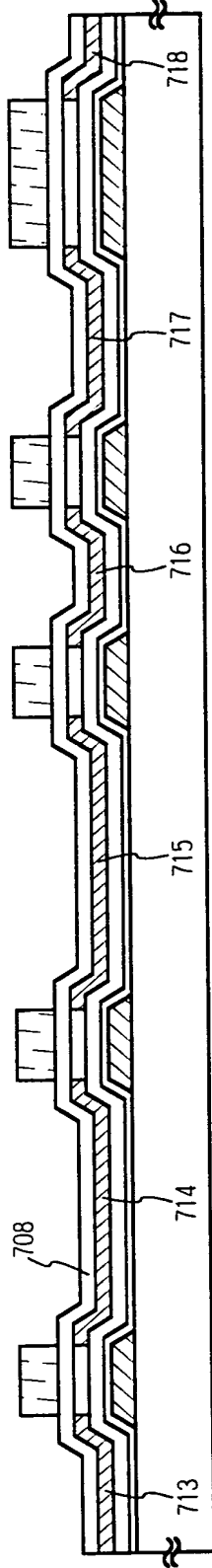


FIG. 7C

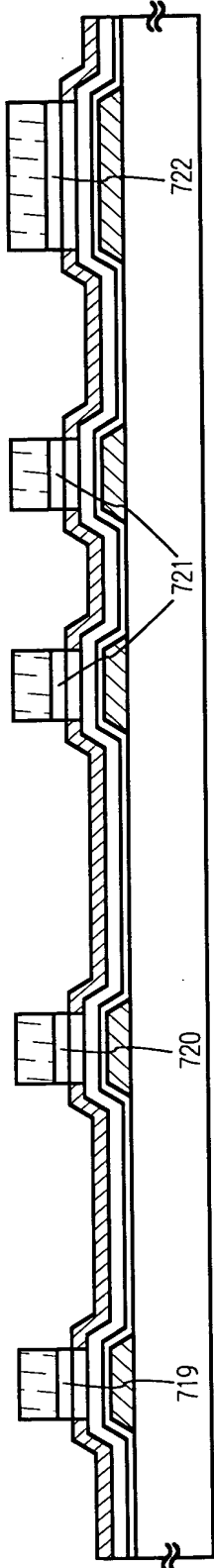


FIG. 8A

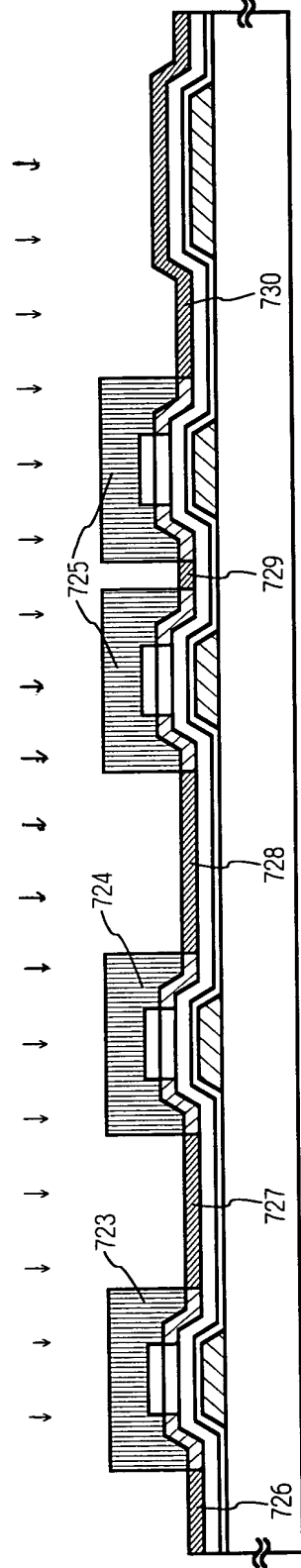


FIG. 8B

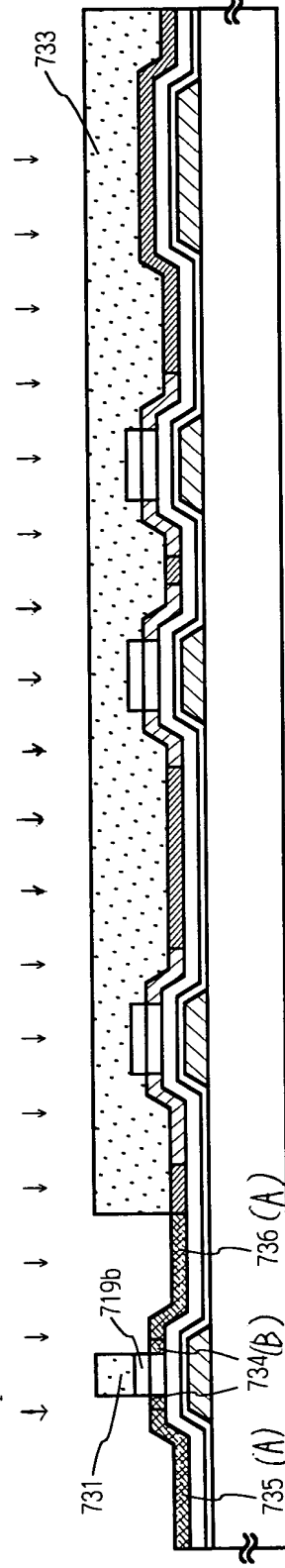


FIG. 8C



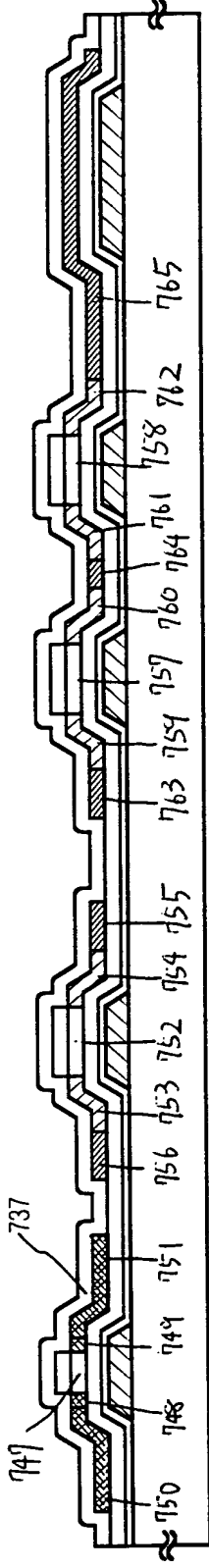


FIG. 9A

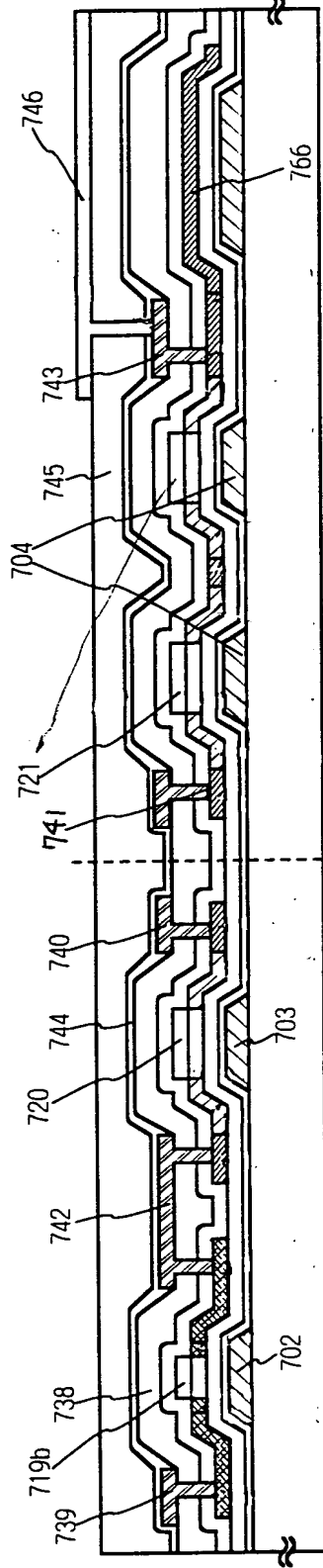


FIG. 9B

769: P-CHANNEL TFT      769: N-CHANNEL TFT      770: CAPACITOR

DRIVER CIRCUIT      PIXEL REGION

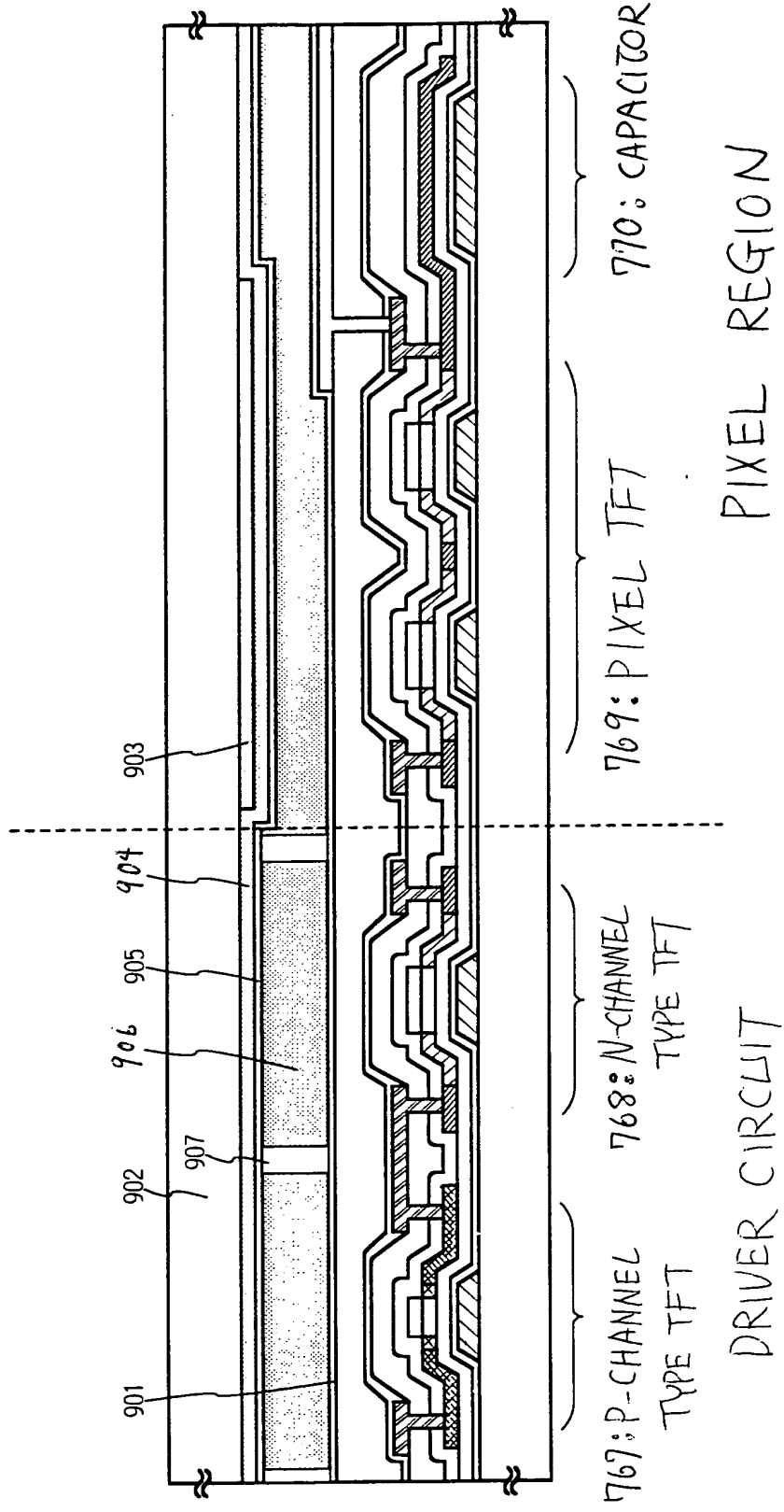


FIG. 10

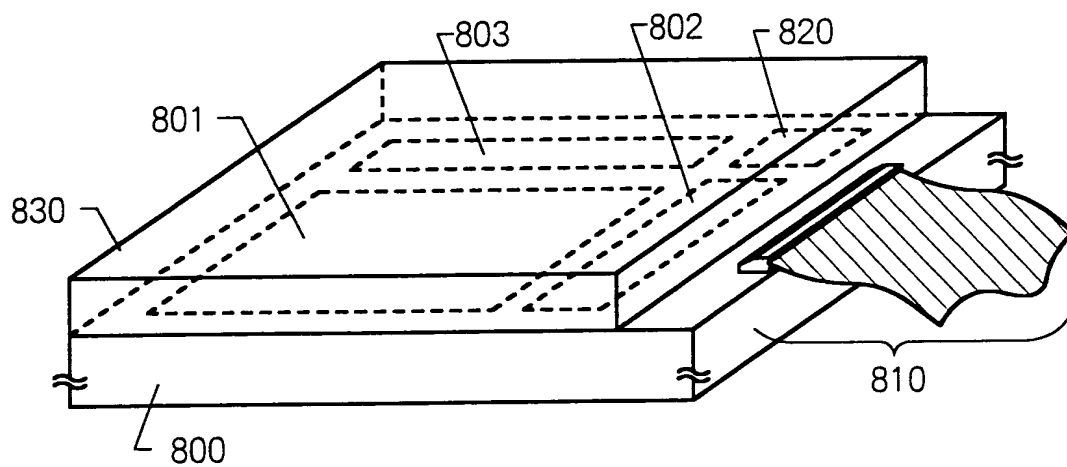


FIG. 11

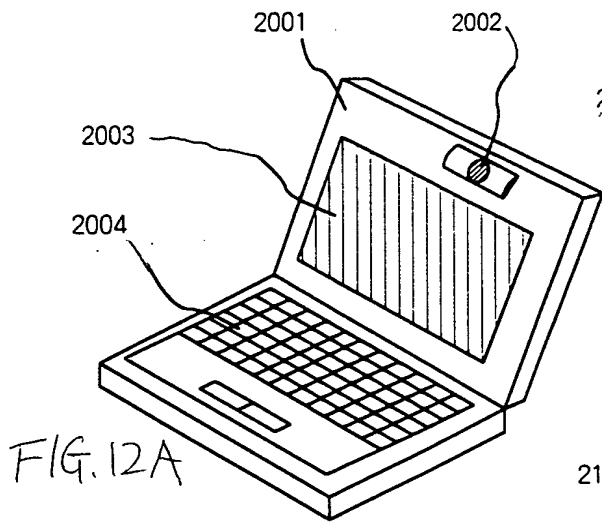


FIG. 12A

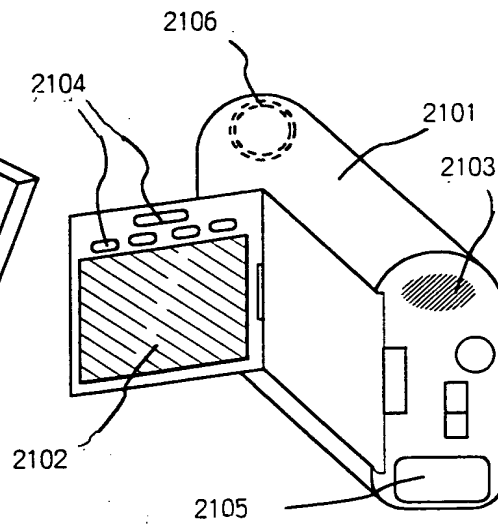


FIG. 12B

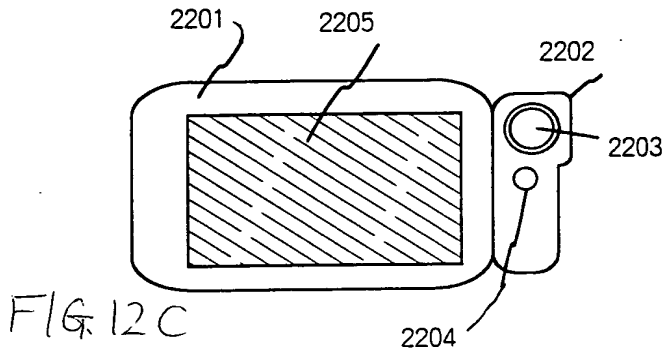


FIG. 12C

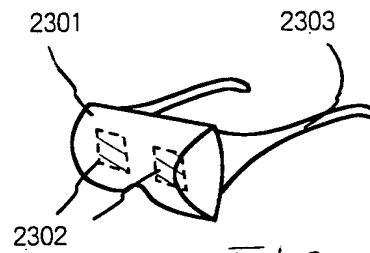


FIG. 12D

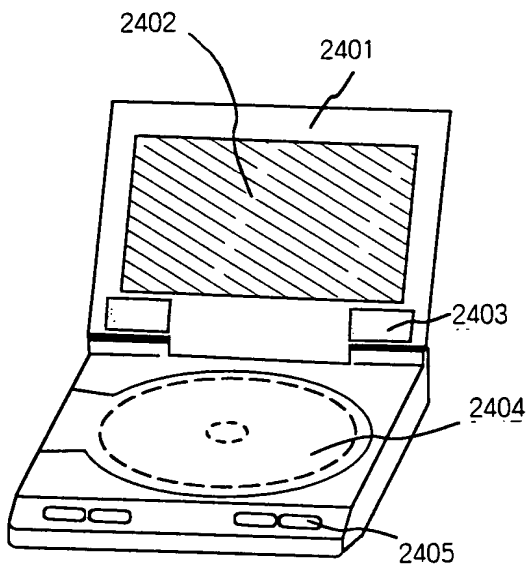


FIG. 12E

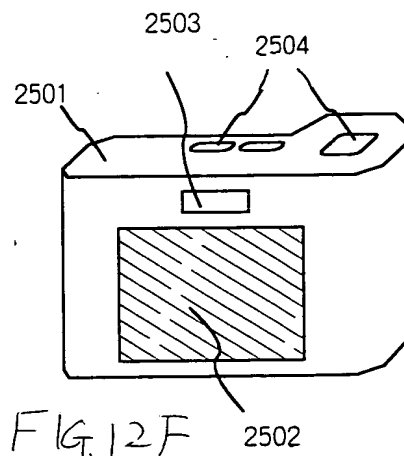


FIG. 12F

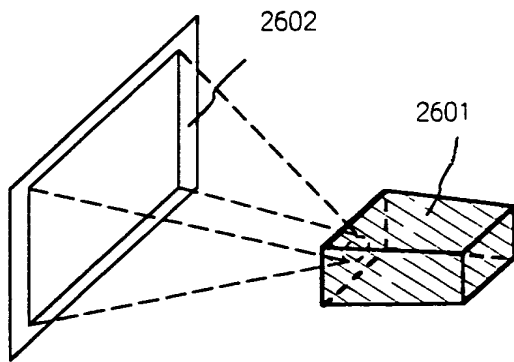


FIG. 13A

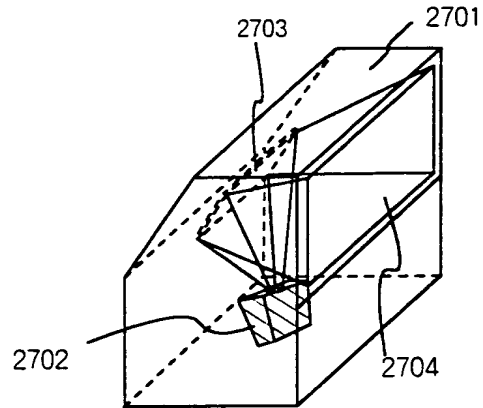


FIG. 13B

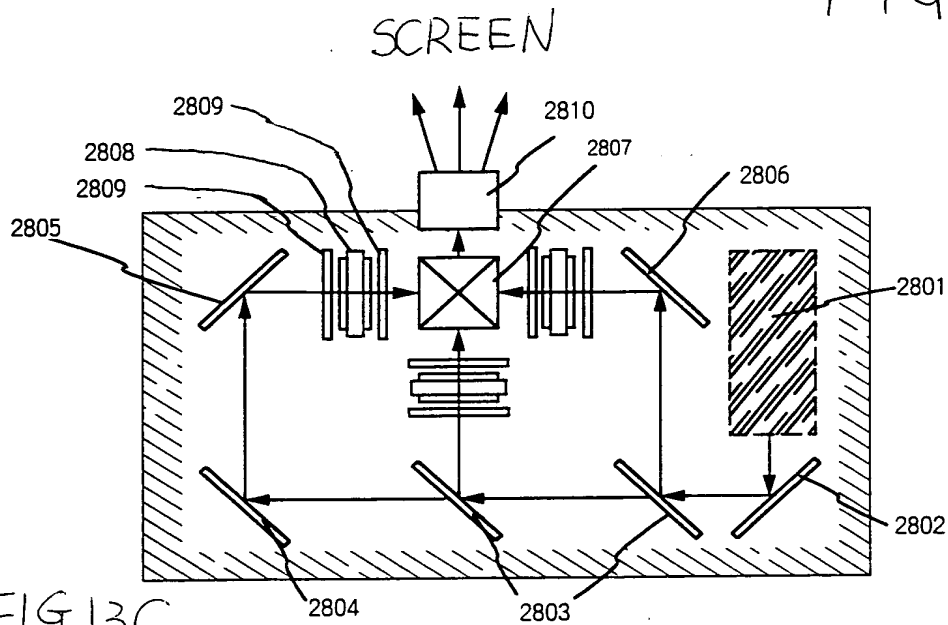


FIG. 13C

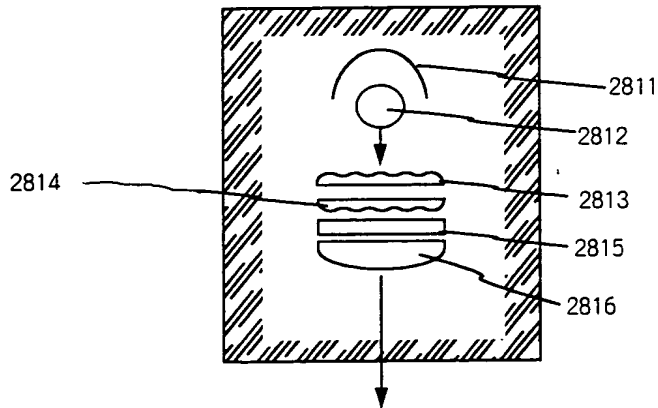
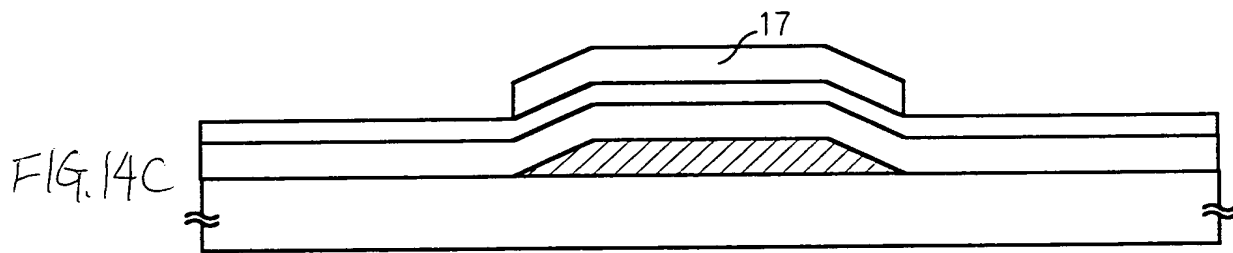
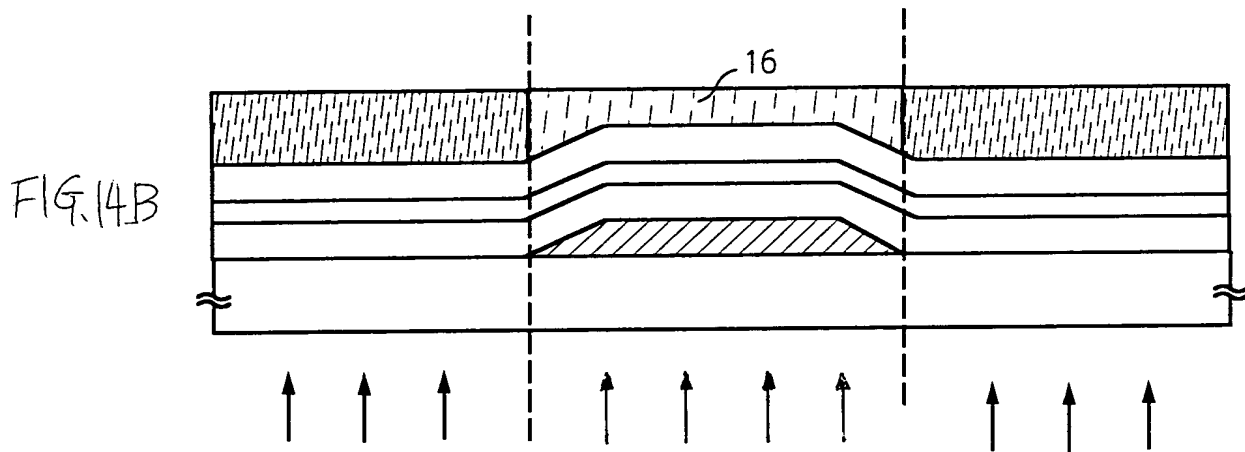
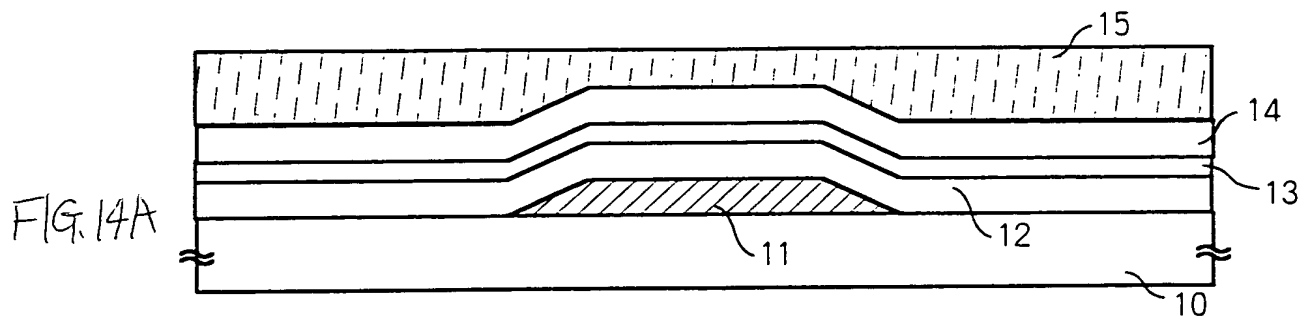
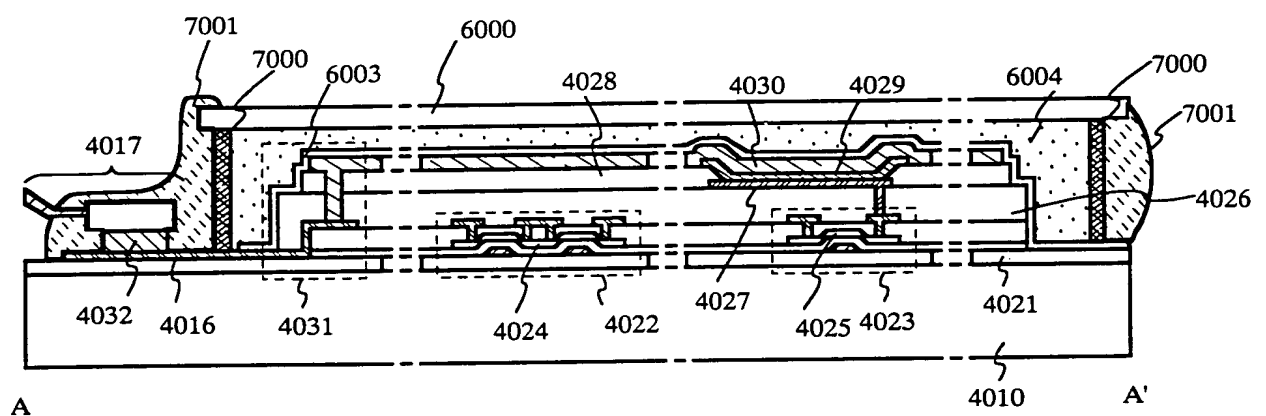
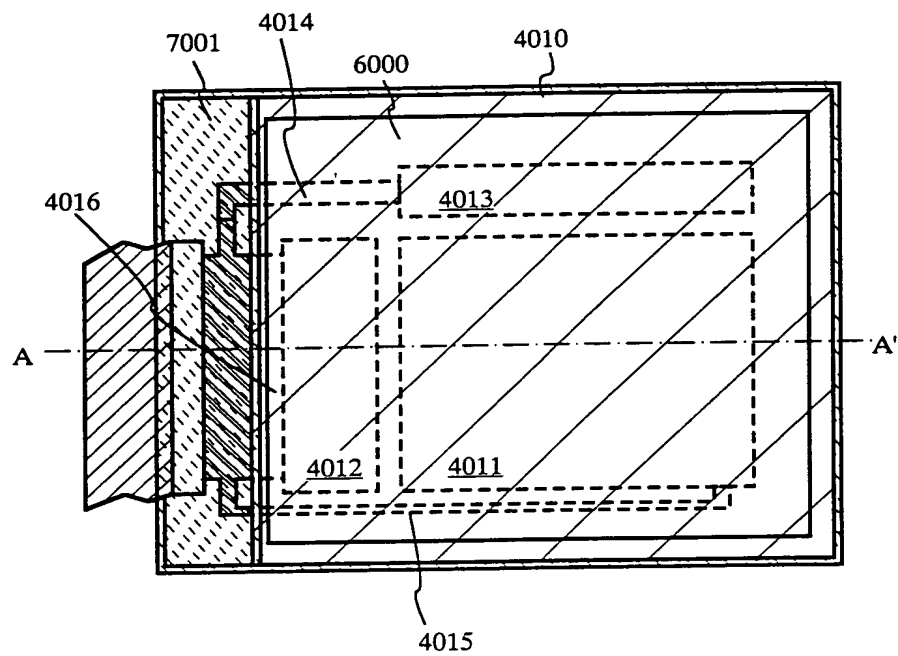


FIG. 13D





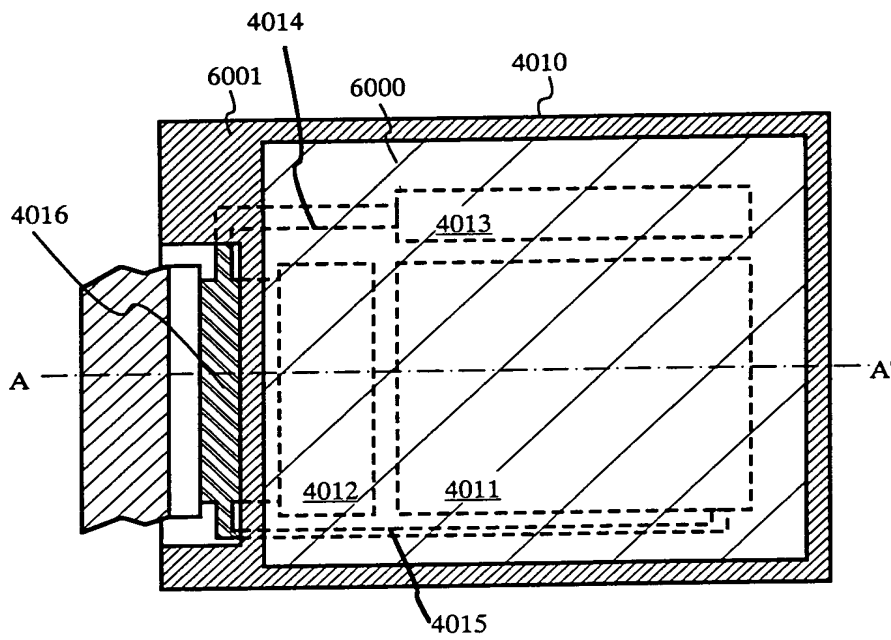


FIG. 16A

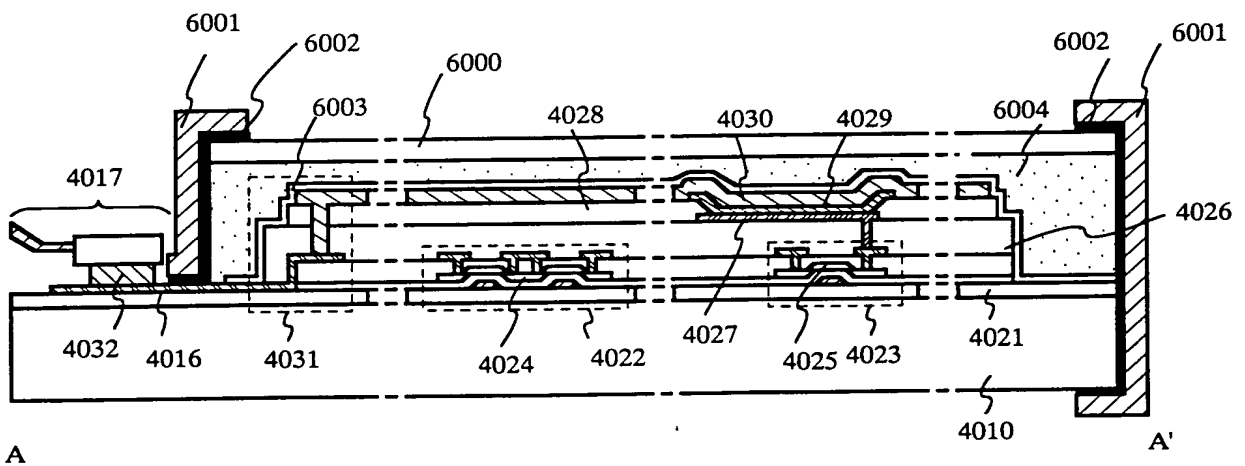
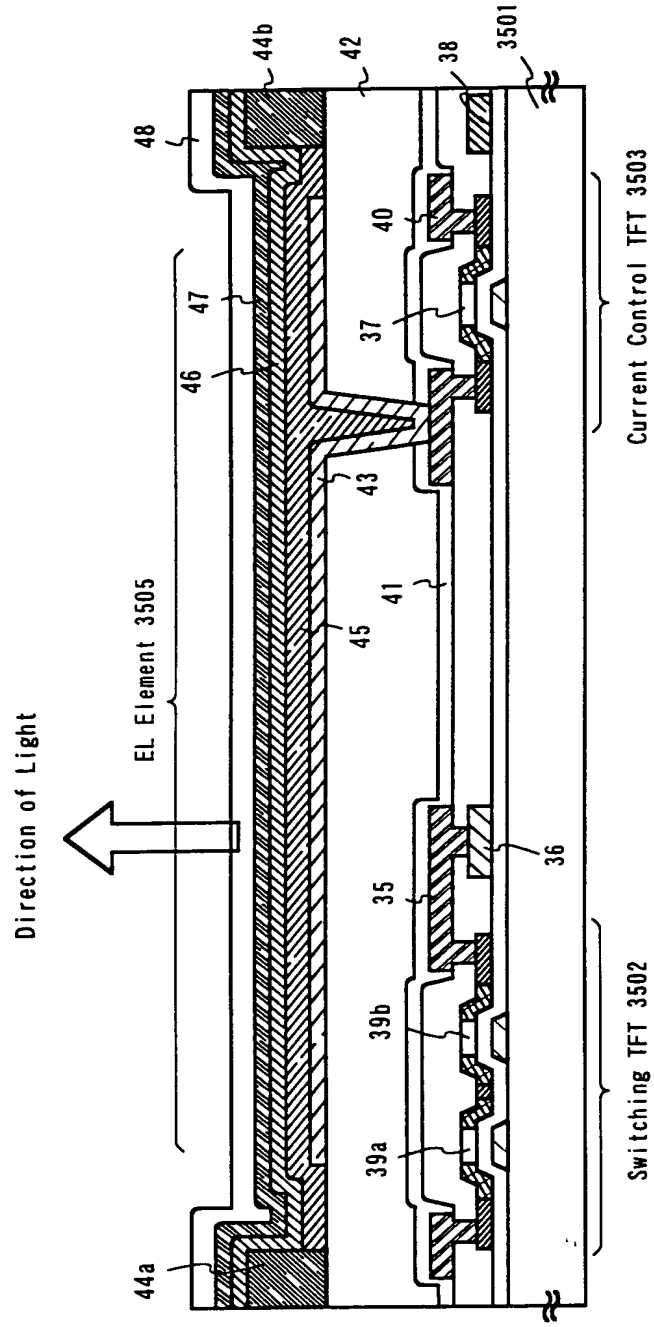


FIG. 16B





71G17

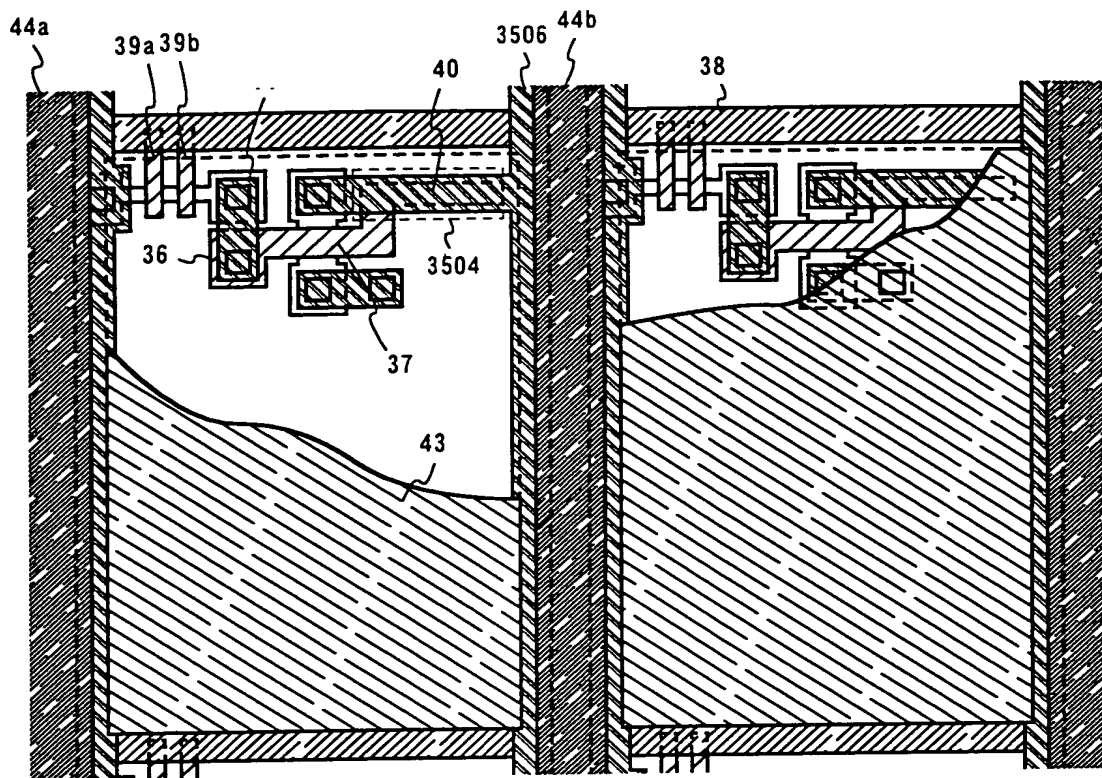


FIG. 18A

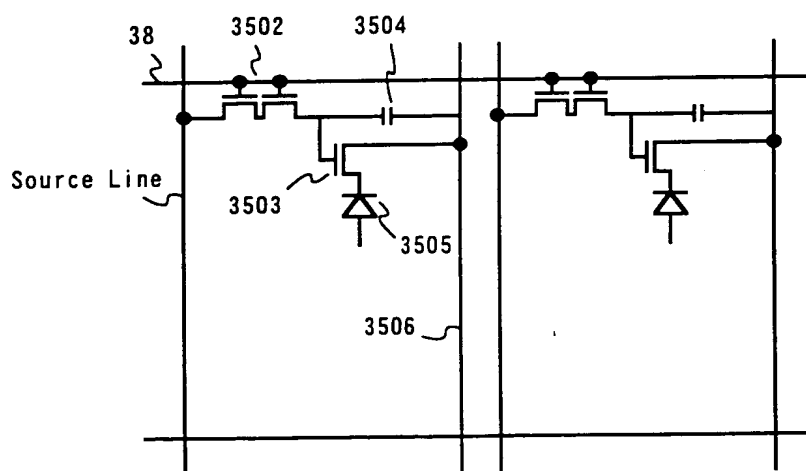


FIG. 18B

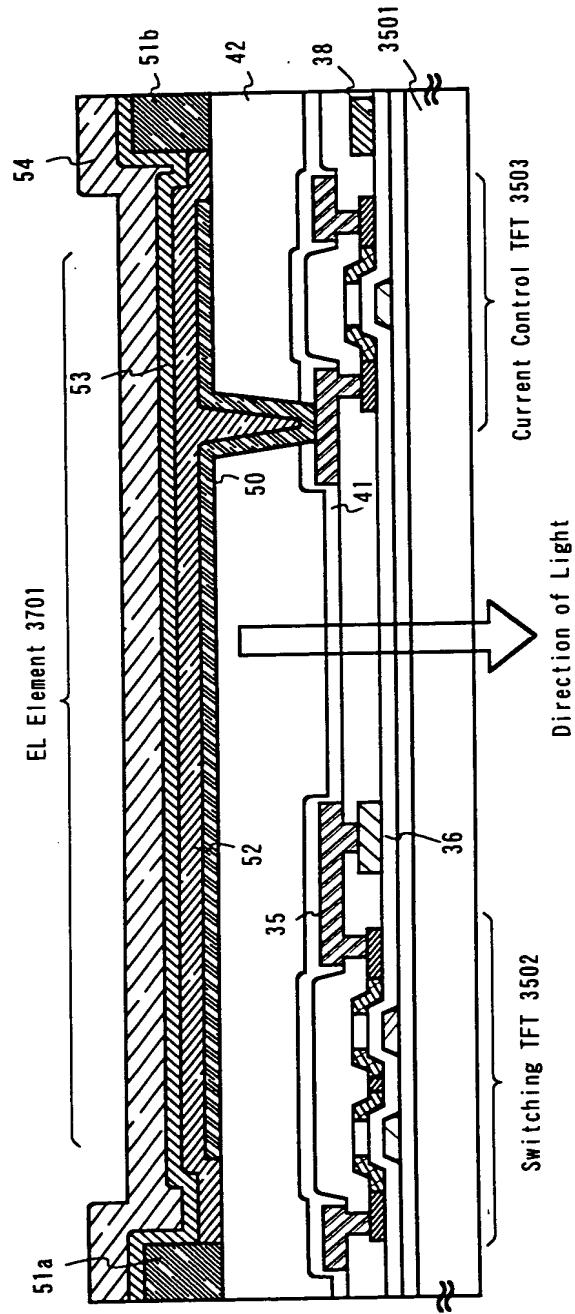


FIG. 19

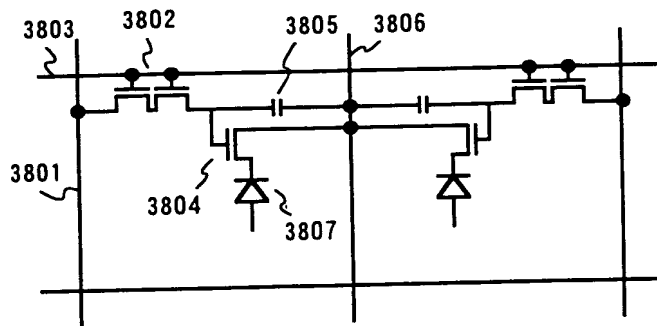


FIG. 20A

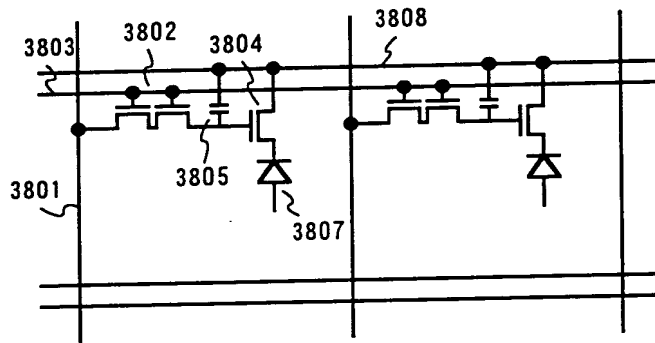


FIG. 20B

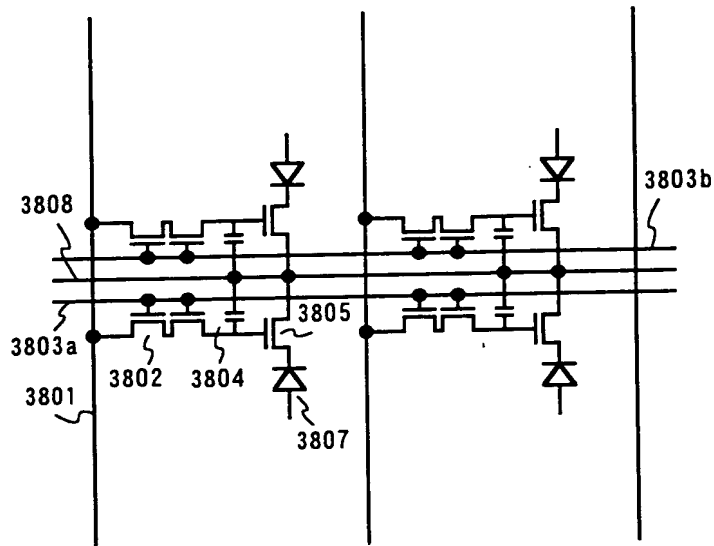


FIG. 20C